

# $\mathsf{G}\,\mathsf{I}\,\mathsf{G}\,\bigwedge\,\mathsf{I}\,\mathsf{O}$

### GIGAIO FABREX SWITCH Extreme Flexibility, Performance and Efficiency



Through an all-new architecture, GigalO offers a hyperperformance network that enables a unified, software-driven composable infrastructure. Disaggregation and composability meet the demands of new data-intensive applications and dynamically assigns resources to match changing workloads.

## The FabreX Switch is the fundamental building block of the FabreX network for true Software Defined Infrastructure (SDI).

The switch communicates with FabreX host drivers to identify and coordinate resources required by the hosts, then quickly connects the respective resources. Choose from a variety of switch software packages to provide the cluster configurations, management and control you need.

Connections between compute, storage and application accelerator resources in the GigalO FabreX network are implemented with the rugged, packetized communication protocol of industry-standard PCI Express.

FabreX networking is administered using DMTF open-source Redfish® APIs that provide an easy-to-use interface for configuring computing clusters on-the-fly.

Choose your hardware: FabreX 24-port switch, FabreX Host Adapter, FabreX Cables, FabreX OS. Then pick the right software:

#### COMPOSER PACK

Enables full disaggregation and dynamic composabliity for deep and wide IO tree configurations. Allows you to create pools of storage and accelerators of all kinds and types and share them betwen servers.

#### LEADER PACK

In addition to composing, this pack provides you with true multi-host communication for small clusters engaged in parallel processing and distributed memory by integrating M{I, NVMe-oF, GDR and TCP-IP.

#### NETWORK PACK

For true composability where you need larger clusters. You'll have all the benefits of the Leader Pack but also the ability to quickly scale your system and dynamically assign resource to meet changing workloads.

#### GET IN TOUCH

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#### EMEA

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#### WHAT DO I GET FROM VSCALER?

#### Performance

Every port of the FabreX Switch interfacing with the Host is equipped with 4-channel DMA engines for full-duplex data traffic. Virtual channels and traffic classes with egress port arbitration contribute to QoS features of the FabreX network. The non-blocking ports feature latency values of 43nsec with x16 link width and 86nsec for x8 link width for higher throughput and the lowest latency in the industry.

#### Flexibility

Upgrade or add compute, storage and application accelerators at the component level that plug-n- play with your environment. There's no need to buy the latest server technology.

The switch can unite a far greater variety of resources, connecting GPUs, TPUs, FPGAs and SoCs to other compute elements or PCI endpoint devices, such as NVMe, PCIe native storage, and other I/O resources. Span multiple servers and multiple racks to scale up single-host systems and scale out multi-host systems, all unified via the FabreX Switch

#### Efficiency

Featuring 100% PCI-SIG compliance, the FabreX switch can integrate heterogenous computing, storage and accelerators into one symmetrical system-area cluster fabric, so you can do more with less. Patented GigalO technology strips away unnecessary conversion, software layers and overheads that add network delay to legacy interconnects.

TRUE DISAGGREGATION DYNAMIC COMPOSAE	N WITH BILITY	SUPER-LOW LATENCY A BANDWIDTH	ND HIGH	100% PCI EXPRESS INTERCONNECT
SPECIFICATIONS				
MANAGAMENT	Open systems FabreX OS with DTMF Redfish Composable APIs			
ARCHITECTURE	Fully disaggregated with dynamic composability			
PORT SIDE	24 Ports non-blocking x4 PCIe Gen3 Link 19 inch, 1u rack mountable			
CONNECTORS	Mini-SAS connectors – short and long range copper and photonics			
LATENCY	24 Ports Non-Blocking Port to port latency from 43ns x16			
BANDWITDH PER PORT	x4 (32 Gbits/sec) Half Duplex, (64 Gbits/sec) Full Duplex			
TOTAL BANDWITDH	Total FabreX Bandwidth 768 Gb/sec Half Duplex, 1,563 Gb/sec Full Duplex			
FAIL OVER	N+1 with multi-switch topologies			

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